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CONFIRMATION NO. 4917

SERIAL NUMBER 10/823,649	FILING OR 371(c) DATE 04/14/2004 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 1076.1094
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APPLICANTS

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** CONTINUING DATA ***** *NOTE, NMD*

** FOREIGN APPLICATIONS ***** *VERIFIED, NMD*
 JAPAN 2003-303495 08/27/2003

IF REQUIRED, FOREIGN FILING LICENSE GRANTED **
 06/25/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 10	TOTAL CLAIMS 25	INDEPENDENT CLAIMS 11
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>[Signature]</i> Examiner's Signature	<i>NMD</i> Initials			

ADDRESS
 21171

TITLE

Method and apparatus for designing semiconductor integrated circuit

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